



STPC Client Process Change

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1. Overview.

This application note describes changes that have occurred during a process change on the STPC Client. Details of the process change are documented in the PCN NVG/66/00-001.

The variations occur at the level of the strap options, the power consumption and PCI Timings.

2. The Strap Options.

The following strap options vary depending on the device that is being used. The detail of which devices are affected in which way is listed in Table 1 below;

TABLE 1. Strap Option Differences

Strap Option	Devices Settings	
	MDBT*70xxxx ¹ (Old)	MDBT*710Axx (New)
MD [32]	Pull Up (1)	Pull Down (0)
MD[35]	Pull Up (1)	Pull Down (0)
Note 1; All devices with the exception of the technical codes; MDBT*S710A The Devices are identified using the technical code which is the first line laser marked under the ST logo.		

This affects frequencies of HCLK when using DMA on the IDE and HCLK at 75MHz with PCICLK at 25 MHz. It is imperative that these values are used or else the system will hang.

3. Power Consumption

With this change of process, the maximum power consumption has changed and is given in Table 2 below;

TABLE 2. Power Consumption Differences

	Devices	
	MDBT*70xxxx ¹ (Old)	MDBT*710Axx (New)
Vdd = 3.3V @ 66MHz	3.26W	3.49W
Vdd = 3.3V @ 75MHz	3.39W	3.97W
Vdd = 3.6V @ 75MHz	4.28W	5.15W
Note 1; All devices with the exception of the technical codes; MDBT*S710A Test conditions; Running Win95, CPU utilisation at 100%, with VIP, PCI, TVOut, IDE, ISA, also being serviced.		

4. Technical Support

STMicroelectronics is on the Internet with a worldwide web (WWW) site on which product presentation, technical literature as well as product support information can be found.

A dedicated STPC section is available providing up to date hardware documentation and software tools.

The Web address is:

WWW: <http://www.st.com/stpc>

APPLICATION NOTE

5. Update History for Application Note AN 1297

This is the first issue.

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